

## Meeting Minutes

February 18, 2008 – 11 AM

Attendees: ECE 492 students and professors

Missing: Serdar

Prepared by: Emily

### Note about Status Letters:

- each letter should have 3 sections: did, will do, didn't do (behind schedule)
- keeping track of hours should have tasks as the rows and names (or initials) as the columns, also keep track of overall hours spent

### Hierarchical design

- should be tree oriented; or top down design
- make diagrams for top level (index), then system block diagram is one step down, then step through each section of the block diagram down to individual components and wires.
- make sure each diagram has names on it, the title, and a number then refer to diagrams based on their diagram number
- create a BOM (bill of materials) columns include: ref des, part #, description, manufacturer, and price.
- Also need to do a hierarchical software design

### Tasks:

- For Wednesday February 20
  - o Top level block diagram
  - o How big will boards be? (estimate it, estimate using through hole)
- For Friday February 22
  - o Design of API
  - o Communication (between PC and PIC) demo
  - o Schematic prototype of station cards and low level boards
  - o Testing that the PIC will be able to do what we want
- For next week
  - o Design of maintenance mode / demo mode
  - o BOM and diagrams