Labor hours by task for the week of 3/3 to 3/8

Task Number		Tom	Alex	Mumo	Jeff	Dan	Evan	Emily	Serdar	Eric	Taha	Shrijan	Jimmy	Hasan	Kelly	Austin	Current Week Totals	From Previous Weeks	TOTAL
	LOW LEVEL																		
133	Train power circuit design	1	1			1.5												8	
134	Switch Control Circuit Design	1.5		2.5		1.5												6.5	
135	Train Detection Circuit Design					1.5												7.5	
136	Overall System Power Distribution	1																11	
138	Software Testing on Spice																		
139	Interface with Network group for parts integration																		
140	Breadboard Testing of Systems			2															
142	Design of PCB Layout				17														
143	PCB Fabrication																		
	Low-Level/Networking Integration & Testing																		
	-Breadboard																		
151	Integrate rail power with PWM subsystem								5	1									
	MANAGEMENT TASKS																		
170	Procurring Materials																		
35	CDR Report		2					4	5.5	5		4	7.5	6	6.4				
36	Draft Users Manual	4																	
39	Draft Acceptance Plan		2																
	NETWORKING																		
63	Design Polling subsystem								2.5										
58	Timing Analysis							1											
59	RS232 communication subsystem																		
60	Packet Parser & Builder																		
61	Design PWM subsystem								3	1									
62	Design Interrupt subsystem																		
65	Design layout of PIC board with feedback systems (LED's etc)								2										
68	Prepare Test Plans																		
72	Test PWM system																		
74	Test Packet parser & builder																		
77	Integrate RS232 communication systems																		
	UI/CONTROLLER GROUP																		
88	Requirements Analysis																		
89	High Level Block Diagram																		
90	Detailed block diagrams (UML)																		
91	Test Bench Descriptions																		
123	Detailed block diagrams (UML)																		
124	Test Bench Descriptions																		
	-Api																		
93	Work with networking group to determine packet structure															1.5			
	Implementation																		
95	Implement storage classes												1			1			
96	Implement main class												1			1			
97	Implement builder decoder												1			1			
	-Demo																		
102	Logic design																		
103	GUI design																		
	-Maintenance																		
108	GUI design																		
	-General																		
115	XML Design and Coding																		