

Labor hours by task for the week of 2/18 to 2/24

Task Number		Tom	Alex	Mumo	Jeff	Dan	Evan	Emily	Serdar	Eric	Taha	Shrijan	Jimmy	Hasan	Kelly	Austin	From	Totals	
																	Previous Weeks		
<b>Low Level</b>																			
116	Train Power Circuit Design	1.5	1.5														5	8	
117	Switch Control Circuit Design			6.5														6.5	
118	Train Detection Circuit Design					4												3.5	7.5
119	Overall System Power Distribution Interface with Network Group for Parts	3	3			5													11
121	Integration				2.5			7			3								12.5
123	Familiarity with PCB Layout Software																	9	9
124	Design of PCB Layout				12			4											16
<b>Networking</b>																			
55	Timing Analysis											2							2
56	PIC Tutorial																	21.5	21.5
57	RS-232 Communication Subsystem						4	1			4							6	15
58	Packet Parser and Builder						1.5		2	6									9.5
59	Design PWM Subsystem						1.5		2	4								3	10.5
60	Design Interrupt System								13.5	1								6	20.5
61	Design I2C Subsystem to Communicate Between PICs								9.5									4	13.5
<b>PC</b>																			
78	Detailed Block Diagrams												1.5	1.5		1			4
79	Test Bench Descriptions																	6	6
81	Maintenance Mode GUI											2	5	2.5	2			1	12.5
82	Demonstration Mode GUI											2	3.5	3	2			0.5	11
99	Interface with UI and Automatic Control and Create API - Packet Builder											0.5	1.5	1.5		5.5		10.5	19.5
104	Interface with UI and Automatic Control and Create API - Packet Decoder											0.5	1.5	1.5		5		10	18.5
<b>Management</b>																			
2	PDR																	44.5	44.5
3	CDR							1											1
130	Management Tasks	1	1	1	1	1	1	1	1	1	5.5	1	1	1	1	2		14.5	35